THIS PAGE BLANK (USPTO)



control.

## MARKUP OF THE AMENDED PARAGRAPHS

## COPY OF PAPERS ORIGINALLY FILED

One page, 1, line 5, the markups are as follows:	
This application is a continuation-in-part of [application 54208-28 filed on	, having SN
and is incorporated by reference herein.] U.S. Patent Application Serial Number	
09/191.443, filed on November 12, 1998, entitled A UNIVERSAL SERIAL BUS (USB) RAM	
ARCHITECTURE FOR USE WITH MICROCOMPUTERS VIA AN INTERFACE	
OPTIMIZED FOR INTEGRATED SERVICES DEVICE NETWORK (ISDN) and is now issued	
as U.S. Patent No. 6,219,736 B1 on April 17, 2001 and is incorporated herein by referen	ce.
One page 1, line 7, the markups are as follows:	
This application is related to [an application entitled "CONFIGURATION] U.S	. Patent
Application Serial Number 09/670,509, filed September 26, 2000, entitled CONFIGUR	ATION
SELECTION FOR USB DEVICE [CONTROLLER", filed on]CO	NTROLLER.
One page 2, line 12, the markups are as follows:	
To answer a lower-volume need, while still retaining a flexible interface, a different type	
of USB device controller was developed by the applicant. This device, a USB-RAM, is described	
in U.S. [Application No]Patent Application Serial Number 09/19	91,443, filed on
November 12, 1998, entitled A UNIVERSAL SERIAL BUS (USB) RAM ARCHITECTURE	
FOR USE WITH MICROCOMPUTERS VIA AN INTERFACE OPTIMIZED I	<u>OR</u>
INTEGRATED SERVICES DEVICE NETWORK (ISDN) and is now issued as U.S. Patent No.	
6,219,736 B1 on April 17, 2001 (54208-00028), which is hereby incorporated by	y reference into
the present application. USB-RAM device controller provides a general solution for connecting	
to the USB by providing an interface to the USB electrical bus and supporting the fixed protocol	
that is associated with all USB applications. The application specific aspects are handled by	

writing packets into a common memory and reading packets from the memory under interrupt

## SUPPORT FOR SERIAL NUMBERS FOR DOCKET NUMBERS SET FORTH IN SPECIFICATION